

Important notice

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On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

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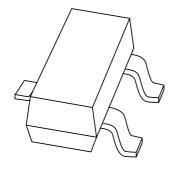
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



PMBT5550 NPN high-voltage transistor

Product data sheet Supersedes data of 1999 Apr 15 2004 Jan 21



NPN high-voltage transistor

PMBT5550

FEATURES

Low current (max. 300 mA)Low voltage (max. 140 V).

APPLICATIONS

• Telephony.

DESCRIPTION

NPN high-voltage transistor in a SOT23 plastic package. PNP complement: PMBT5401.

MARKING

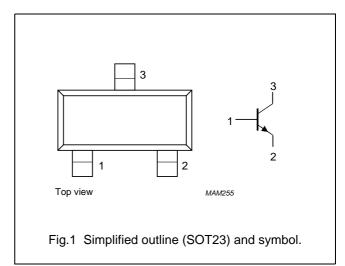
TYPE NUMBER	MARKING CODE(1)
PMBT5550	*1F

Note

1. * = p : Made in Hong Kong.* = t : Made in Malaysia.* = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE				
NUMBER	NAME	DESCRIPTION VERSION			
PMBT5550	_	plastic surface mounted package; 3 leads SOT23			

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	160	V
V _{CEO}	collector-emitter voltage	open base	_	140	V
V_{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)		_	300	mA
I _{CM}	peak collector current		_	600	mA
I _{BM}	peak base current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

NPN high-voltage transistor

PMBT5550

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

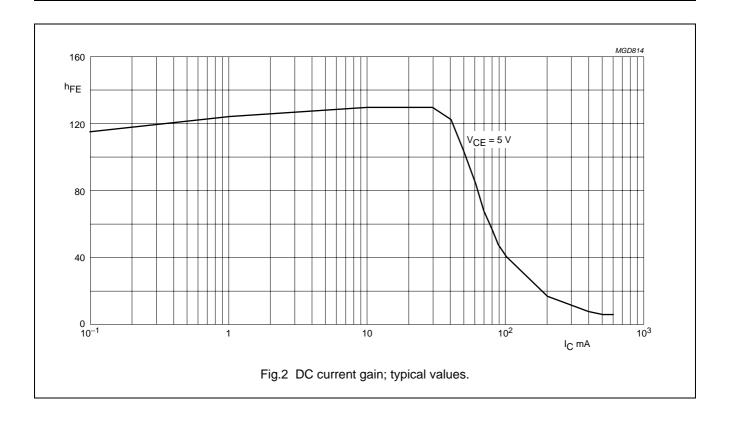
CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = 100 V	_	50	nA
		I _E = 0; V _{CB} = 100 V; T _{amb} = 100 °C	_	50	μΑ
I _{EBO}	emitter-base cut-off current	I _C = 0; V _{EB} = 4 V	_	50	nA
h _{FE}	DC current gain	V _{CE} = 5 V; (see Fig.2)			
		$I_C = 1 \text{ mA}$	60	_	
		$I_C = 10 \text{ mA}$	60	250	
		$I_C = 50 \text{ mA}$	20	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 1 mA	_	150	mV
		$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	_	250	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 1 \text{ mA}$	_	1	V
		$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	_	1.2	V
C _c	collector capacitance	$I_E = I_e = 0$; $V_{CB} = 10 \text{ V}$; $f = 1 \text{ MHz}$	_	6	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	_	30	pF
f _T	transition frequency	I _C = 10 mA; V _{CE} = 10 V; f = 100 MHz	100	300	MHz
F	noise figure	I_C = 200 μA; V_{CE} = 5 V; R_S = 2 kΩ; f = 10 Hz to 15.7 kHz	_	10	dB

NPN high-voltage transistor

PMBT5550



NPN high-voltage transistor

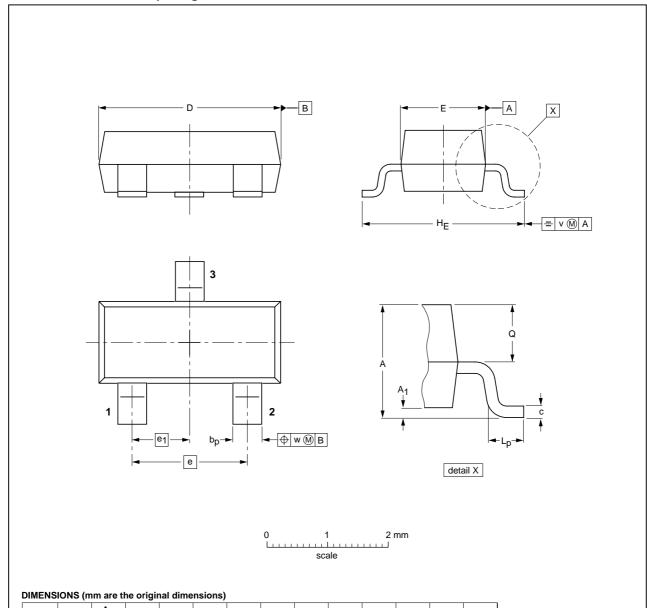
PMBT5550

PACKAGE OUTLINE

UNIT

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

e₁

1.9

 H_{E}

 $\mathbf{L}_{\mathbf{p}}$

0.45

0.55

0.1

2004 Jan 21 5

bp

0.38

max

0.9

NPN high-voltage transistor

PMBT5550

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

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